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Application No.: 10/828,408 Filing Date: April 19, 2004

Inventor: LAKSHMANAN, Viswanathan

Art Unit: 2825 Confirmation No.: 4517

Examiner: Parihar, Suchin

From: Eric J. Whitesell #38657

Today's Date: July 17, 2006 Attorney Docket No.: 03-2644 81693

Pages: 7 pages (including this 1-page coversheet)

Dispatched by: Eric J. Whitesell

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Response to Notice of Non-compliant Amendment (5 pgs.)

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	L	Art Unit	2825	JUL 17 2	
(to be used for all correspondence after initial filing)		Examiner Name	Parihar.	Parihar, Suchin	
Total Number of Pages in This Submission	7	Attorney Docket Number	03-264	4 81693	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): LAKSHMANAN,

Viswanathan et al.

Serial No.: 10/828,408

Filed:

April 19, 2004

METHOD AND COMPUTER For:

> PROGRAM FOR VERIFYING AN INCREMENTAL CHANGE TO AN)

INTEGRATED CIRCUIT DESIGN

Art Unit: 2825

Examiner: Parihar, Suchin

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